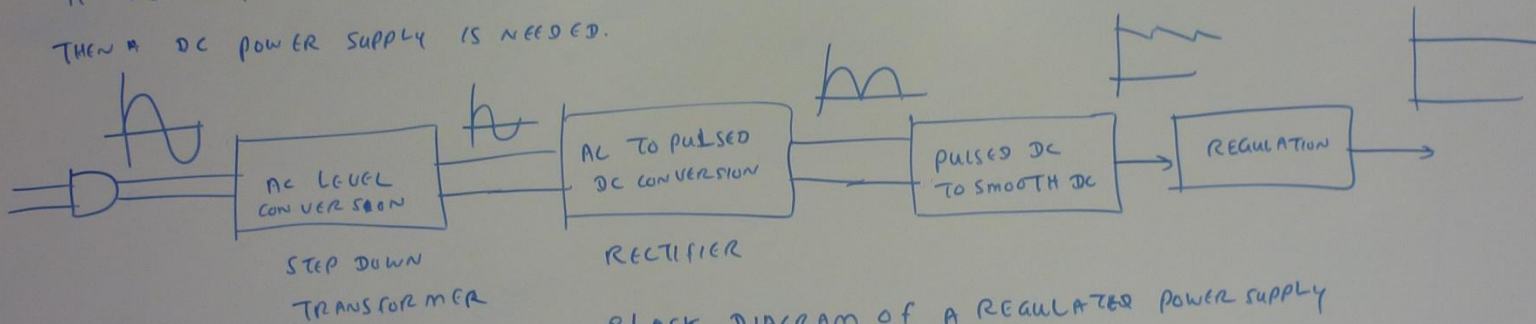


Power Supplies PART (1)

MOST ELECTRONIC EQUIPMENTS REQUIRE SOME FORM OF DC POWER TO OPERATE.

IF THIS EQUIPMENT IS TO BE OPERATED FROM A STANDARD AC OUTLET THEN A DC POWER SUPPLY IS NEEDED.

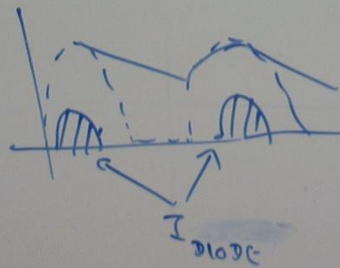
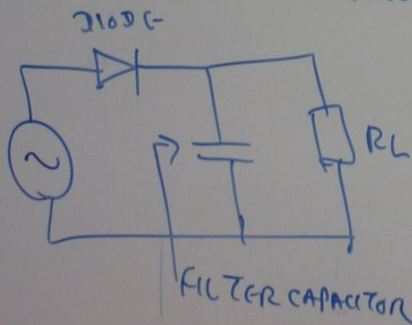


BLOCK DIAGRAM OF A REGULATOR POWER SUPPLY

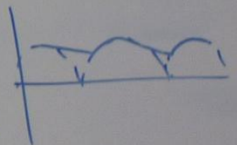
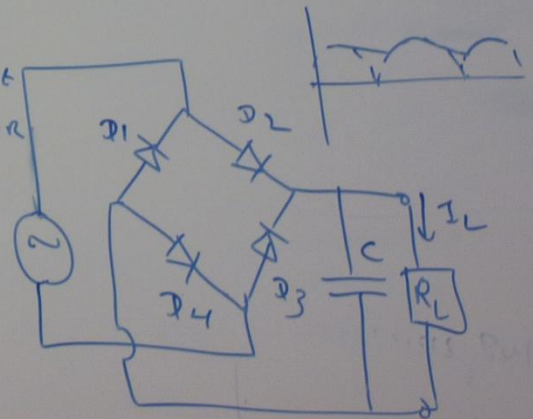
REGULATED power supply

- HALF WAVE RECTIFIER
- FULL WAVE RECTIFIER

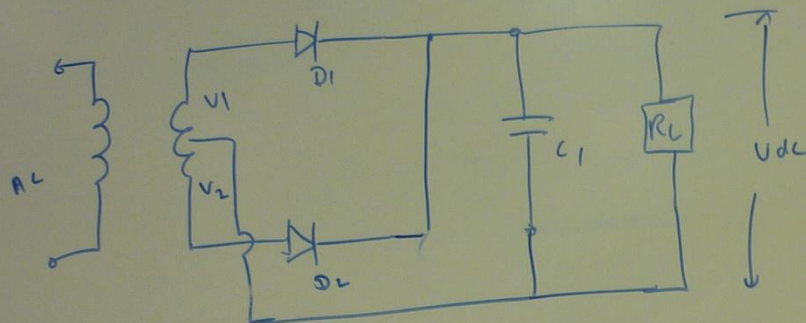
HALF WAVE RECTIFIER



FULL WAVE RECTIFIER



THE CENTRE TAPPED FULL WAVE RECTIFIER



$$V_1 = V_2$$

$$V_{dc} = V_r \text{ for } R_L C$$

V_{dc} = DC OUT PUT VOLTAGE

V_r = PEAK TO PEAK RIPPLE VOLTAGE

f_r = RIPPLE FREQUENCY

C = CAPACITANCE

R_L = LOAD SEEN BY CAPACITOR

P7) CALCULATE THE LOAD RESISTANCE AND CAPACITOR SIZE OF A FULL WAVE RECTIFIER THAT SUPPLIES A 40V DC WITH 2% RIPPLE VOLTAGE (PEAK TO PEAK) AT 250 mA TO A RESISTIVE LOAD. ASSUME THE RECTIFIER IS SUPPLIED WITH 50 Hz AC.

$$R_L = \frac{V_o}{I_L} = \frac{40}{250 \times 10^{-3}} = 160 \Omega$$

$$V_r = 2\% \times \text{DC VOLTAGE}$$

$$= \frac{2}{100} \times 40 = 0.8 \text{ V}$$

$$V_{dc} = V_r \text{ for } R_L C$$

$$40 = 0.8 \times 100 \times 100 \times C$$

$$C = \frac{40}{0.8 \times 100 \times 100} = 3125 \times 10^{-6} \text{ F}$$

$$= 3125 \mu\text{F}$$

$$f_r = 2 f_{\text{supply}}$$

$$f_r = 2 \times 50$$

$$= 100 \text{ Hz}$$

CIRCUIT

1/2 wave
BRIDGE

C.T Full
wave

CIRCUIT	V_{dc}	RIPPLE FREQUENCY
$\frac{1}{2}$ WAVE	V_{max}	f_{supply}
BRIDGE	V_{max}	$2 f_{supply}$
C.T Full WAVE	V_{max}	$2 f_{supply}$

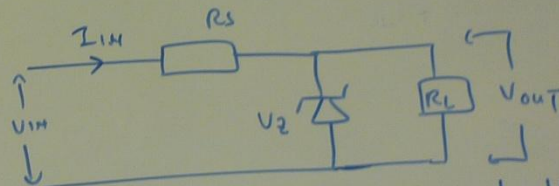
LOAD REGULATION

$$\% \text{ LOAD REGULATION} = \frac{V_{dc}(\text{NO LOAD}) - V_{dc}(\text{FULL LOAD})}{V_{dc}(\text{FULL LOAD})} \times 100$$

POWER SUPPLIES - PART (2)

THE PURPOSE OF VOLTAGE REGULATION IS TO MAINTAIN A CONSTANT VOLTAGE ACROSS A LOAD REGARDLESS OF ANY CHANGE IN LOAD CONDITION OR SUPPLY VOLTAGE.

THE BASIC REGULATING DEVICE IS ZENER DIODE.



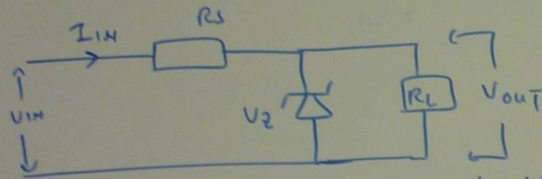
THE OUTPUT VOLTAGE ACROSS LOAD L IS EQUAL TO ZENER VOLTAGE V_Z .

THE OUTPUT VOLTAGE IS KEPT CONSTANT DESPITE CHANGES IN V_{IN} AND I_L BECAUSE THE ZENER CURRENT VARIES TO CHANGE THE VOLTAGE ACROSS THE SERIES RESISTOR R_S TO COMPENSATE.

POWER SUPPLIES - PART (2)

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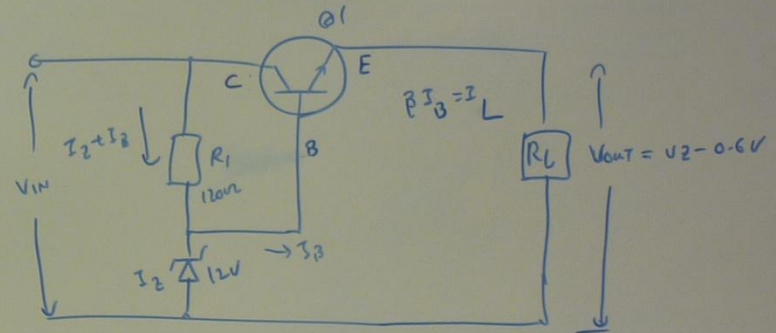
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SERIES TRANSISTOR REGULATOR

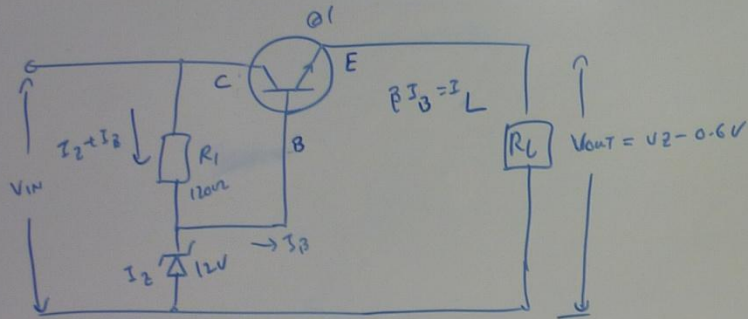


AN INCREASE IN THE LOAD CURRENT WILL CAUSE THE ZENER CURRENT TO REDUCE AS MORE BASE CURRENT IS REQUIRED BY THE TRANSISTOR.

IF THE INPUT VOLTAGE CHANGES, THE ZENER CURRENT WILL ALSO CHANGE, BUT THE ZENER VOLTAGE WILL REMAIN CONSTANT KEEPING THE OUTPUT CONSTANT.

THE INPUT VOLTAGE MUST BE HIGHER THAN REQUIRED OUTPUT VOLTAGE BY AT LEAST 2V.

SERIES TRANSISTOR REGULATOR

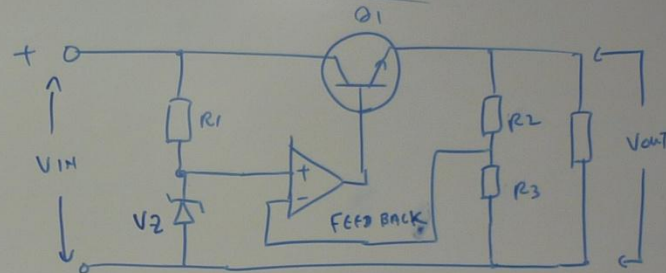


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REGULATOR WITH FEED BACK

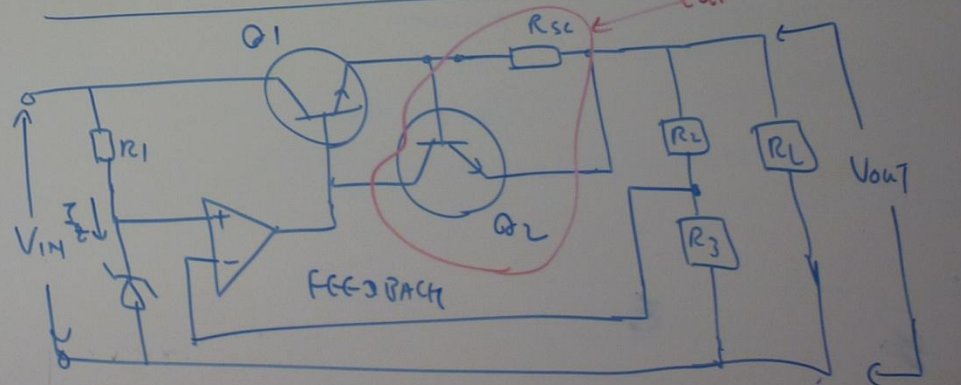


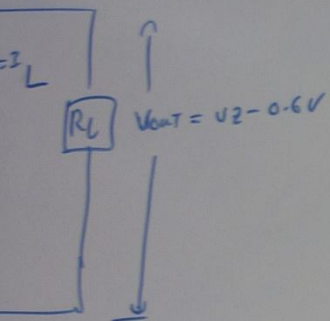
$$V_{OUT} = \left(\frac{R_2}{R_3} + 1 \right) \times V_{REF}$$

$$V_{REF} = V_Z$$

$$\text{POWER DISSIPATION } (P_d) = (V_{IN} - V_{OUT}) I_L$$

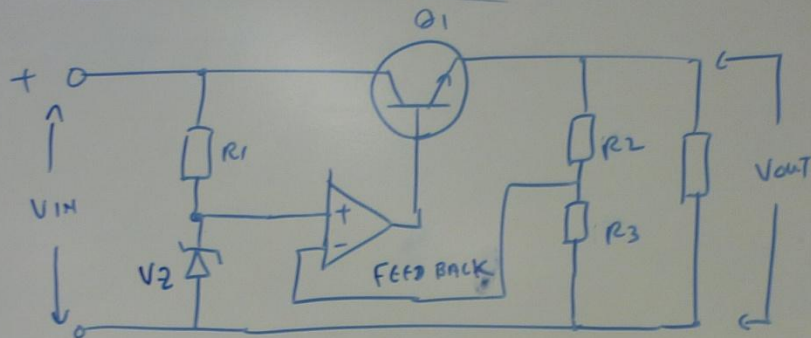
SERIES REGULATOR WITH CURRENT LIMITING





CURRENT WILL CAUSE
 AS MORE
 BY THE TRANSISTOR.
 ES, THE ZENER
 BUT THE ZENER
 CURRENT KEEPING
 HIGHER THAN
 BY AT LEAST 2V.

REGULATOR WITH FEED BACK

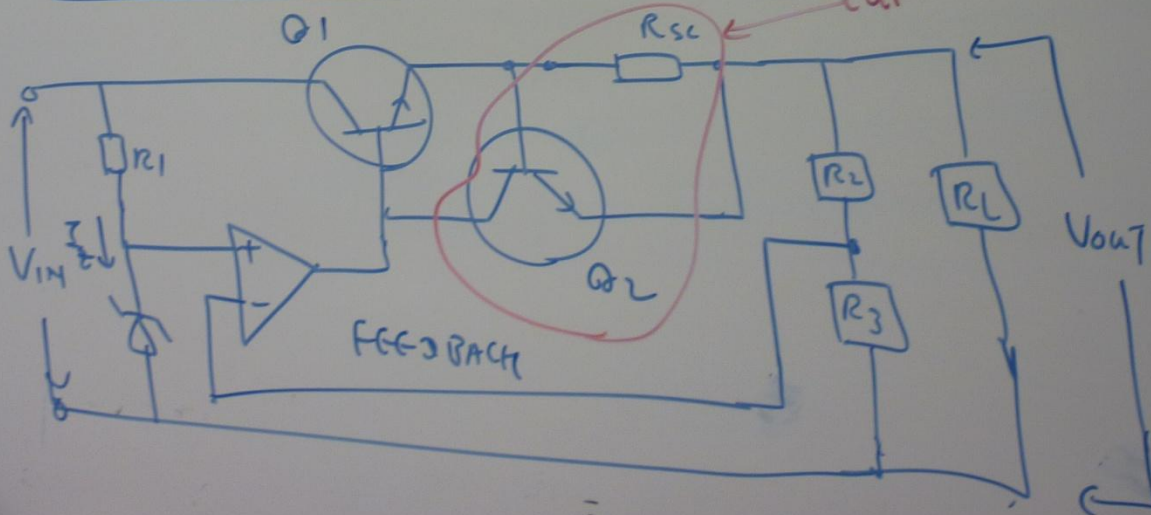


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SERIES REGULATOR WITH CURRENT LIMITING



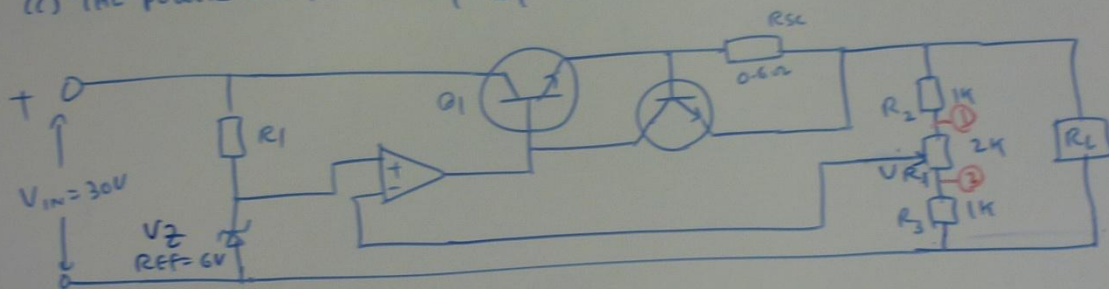
Ph

FOR THE GIVEN CIRCUIT, CALCULATE

(a) THE MAXIMUM AND MINIMUM OUTPUT VOLTAGES

(b) THE CURRENT THAT WILL FLOW IF THE OUTPUT IS SHORT CIRCUITED

(c) THE POWER DISSIPATED BY Q_1 IF THE OUTPUT IS SHORT CIRCUITED



$$V_{OUT} = \left(\frac{R_2}{R_3} + 1 \right) \times V_{REF}$$

$$V_{REF} = V_Z = 6V$$

$$V_{R1} \text{ AT POSITION ① } R_2 = 1k\Omega, R_3 = 3k\Omega$$

$$V_{OUT} = \left(\frac{1}{3} + 1 \right) \times 6 = 8V$$

$$V_{R1} \text{ AT POSITION ② } R_2 = 3k\Omega, R_3 = 1k\Omega$$

$$V_{OUT} = \left(\frac{3}{1} + 1 \right) \times 6 = 24V$$

$$I_{sc} = \frac{0.6}{R_{sc}}$$

$$= \frac{0.6}{0.6}$$

(MAXIMUM POWER)

$$P_d = (V_{IN} - 0)$$

$$= (30 - 0)$$

$$= 30W$$

THREE TERM

78XX,

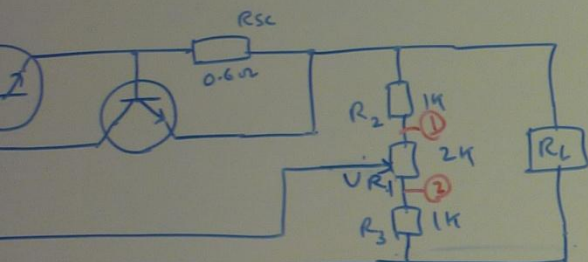
LM317

ULATE

OUT VOLTAGES

IF THE OUT PUT IS SHORT CIRCUITED

IF THE OUT PUT IS SHORT CIRCUITED



$\times V_{REF}$

$V_{REF} = V_Z = 6V$

$R_2 = 1k\Omega, R_3 = 3k\Omega$

$(+1) \times 6 = 8V$

$R_2 = 3k, R_3 = 1k$

$(+1) \times 6 = 24V$

$$I_{sc} = \frac{0.6}{R_{sc}}$$

$$= \frac{0.6}{0.6} = 1A$$

(MAXIMUM POWER DISSIPATION \rightarrow OUT PUT SHORT CIRCUIT
 $V_{out} = 0, I_{sc} = I_L$)

$$P_d = (V_{in} - V_{out}) \times I_{sc}$$

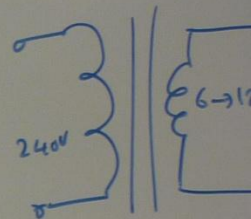
$$= (30 - 0) \times 1$$

$$= 30W$$

THREE TERMINALS REGULATORS

78XX, 79XX, 78H05

LM317



- 1 - IN PUT
- 2 - common
- 3 - OUT PUT

MAXIMUM P

IN PUT VO

OUT PUT

POWER DIS

$$I_{sc} = \frac{0.6}{R_{sc}}$$

$$= \frac{0.6}{0.6} = 1 \text{ A}$$

(MAXIMUM POWER DISSIPATION \rightarrow OUT PUT SHORT CIRCUIT
 $V_{out} = 0, I_{sc} = I_L$)

$$P_d = (V_{in} - V_{out}) \times I_{sc}$$

$$= (30 - 0) \times 1$$

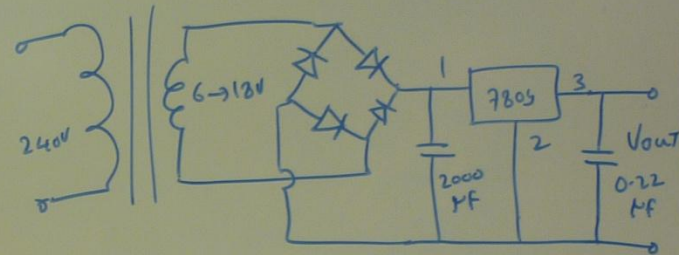
$$= 30 \text{ W}$$

THREE TERMINALS REGULATORS

78XX, 79XX, 78H05

LM317

5 V POWER SUPPLY



- 1 - INPUT
- 2 - common
- 3 - output

MAXIMUM RATINGS

INPUT VOLTAGE = 35V

OUTPUT CURRENT = 1A

POWER DISSIPATION - INTERNALLY LIMITED.

py CALCULATING
 SIZE OF A PA
 40V DC WIT
 AT 250 mA
 RECTIFIER IS

$$R_L = -$$

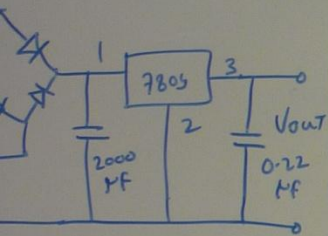
$$V_r = 2$$

$$V_{dc} =$$

$$40 =$$

$$C =$$

POWER SUPPLY



$V = 35V$

$I = 1A$

ON - INTERNALLY LIMITED.

Q7) CALCULATE THE LOAD RESISTANCE AND CAPACITOR SIZE OF A FULL WAVE RECTIFIER THAT SUPPLIES A 40V DC WITH 2% RIPPLE VOLTAGE (PEAK TO PEAK) AT 250 mA TO A RESISTIVE LOAD. ASSUME THE RECTIFIER IS SUPPLIED WITH 50 Hz AC.

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$$V_r = 2\% \times \text{DC VOLTAGE}$$

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$$V_{DC} = V_r f_r R_L C$$

$$40 = 0.8 \times 100 \times 160 \times C$$

$$C = \frac{40}{0.8 \times 100 \times 160} = 3125 \times 10^{-6} F$$

$$= 3125 \mu F$$

$$f_r = 2 f_{\text{supply}}$$

$$f_r = 2 \times 50$$

$$= 100 \text{ Hz}$$

CIRCUIT	V_{DC}	RIPPLE FREQUENCY
$\frac{1}{2}$ WAVE	V_{max}	f_{supply}
BRIDGE	V_{max}	$2 f_{\text{supply}}$
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LOAD REGULATION

$$\% \text{ LOAD REGULATION} = \frac{V_{DC}(\text{NO LOAD}) - V_{DC}(\text{FULL LOAD})}{V_{DC}(\text{FULL LOAD})} \times 100$$